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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: D. Sasaki, et al.

Serial No.: 09/922,384

Filed: August 3, 2001

For: MOUNTING STRUCTURE OF A  
SEMICONDUCTOR DEVICE, AND A  
PROCESS FOR MOUNTING A  
SEMICONDUCTOR DEVICE ON A  
MOUNTING SUBSTRATE

Case No.: 9792909-5175

Group Art Unit: 3729

Examiner: not yet assigned

Certificate of Mailing (37 CFR 1.8(a))

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the:

Box Patent Application  
Attn.: IPE  
Assistant Commissioner for Patents  
Washington, D.C. 20231, on:

Date of Deposit: October 19, 2001

JoEllen Hogan

Date

**RESPONSE TO NOTICE TO FILE CORRECTED APPLICATION PAPERS**

Box Patent Application

Attn.: IPE

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

In response to the Notice to File Corrected Application Papers (Filing Date granted), dated August 21, 2001, in the above-identified patent application, applicants submit herewith a substitute specification in compliance with 37 CFR 1.52.

The Commissioner is hereby authorized to charge any additional fees required, as well as any patent application processing fees associated with this communication for which full payment has not been tendered, to Deposit Account No. 19-3140. A duplicate copy of this sheet is enclosed.

Respectfully submitted,  
SONNENSCHN NATH & ROSENTHAL

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David R. Metzger  
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Washington, D.C. 20231, on:

Date of Deposit: October 19, 2001

*Jo Ellen Hogan*  
Jo Ellen Hogan

*10/19/01*  
Date

**TRANSMITTAL LETTER**

Box Patent Application  
Attn.: IPE  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

Enclosed herewith is a Response to Notice to File Corrected Application Papers of D. Sasaki, et al. in the above-identified patent application entitled MOUNTING STRUCTURE OF A SEMICONDUCTOR DEVICE, AND A PROCESS FOR MOUNTING A SEMICONDUCTOR DEVICE ON A MOUNTING SUBSTRATE..

Also enclosed are: Substitute Specification  
Copy of Notice to File Corrected Application Papers  
Return Receipt Postcard

The Commissioner is hereby authorized to charge any additional fees required, as well as any patent application processing fees associated with this communication for which full payment has not been tendered, to Deposit Account No. 19-3140. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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FEB 04 2002

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
WWW.USPTO.GOV

APPLICATION NUMBER	FILING RECEIPT DATE	FIRST NAMED APPLICANT	ATTORNEY DOCKET NUMBER
09/922,384	08/03/2001	Dai Sasaki	9792909-5175

CONFIRMATION NO. 8116

FORMALITIES LETTER



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26263  
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WACKER DRIVE STATION  
CHICAGO, IL 60606-1080

Date Mailed: 08/21/2001

## NOTICE TO FILE CORRECTED APPLICATION PAPERS

### Filing Date Granted

This application has been accorded an Application Number and Filing Date. The application, however, is informal since it does not comply with the regulations for the reason(s) indicated below. Applicant is given **TWO MONTHS** from the date of this Notice within which to correct the informalities indicated below. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a)

The required item(s) identified below must be timely submitted to avoid abandonment:

- A substitute specification in compliance with 37 CFR 1.52 because:
  - Papers contain improper margins. *Each sheet must have a left margin of at least 2.5 cm (1") and top, bottom and right margins of at least 2.0 cm (3/4")*

*A copy of this notice **MUST** be returned with the reply.*

Customer Service Center

Initial Patent Examination Division (703) 308-1202

PART 2 - COPY TO BE RETURNED WITH RESPONSE